

Figure 1

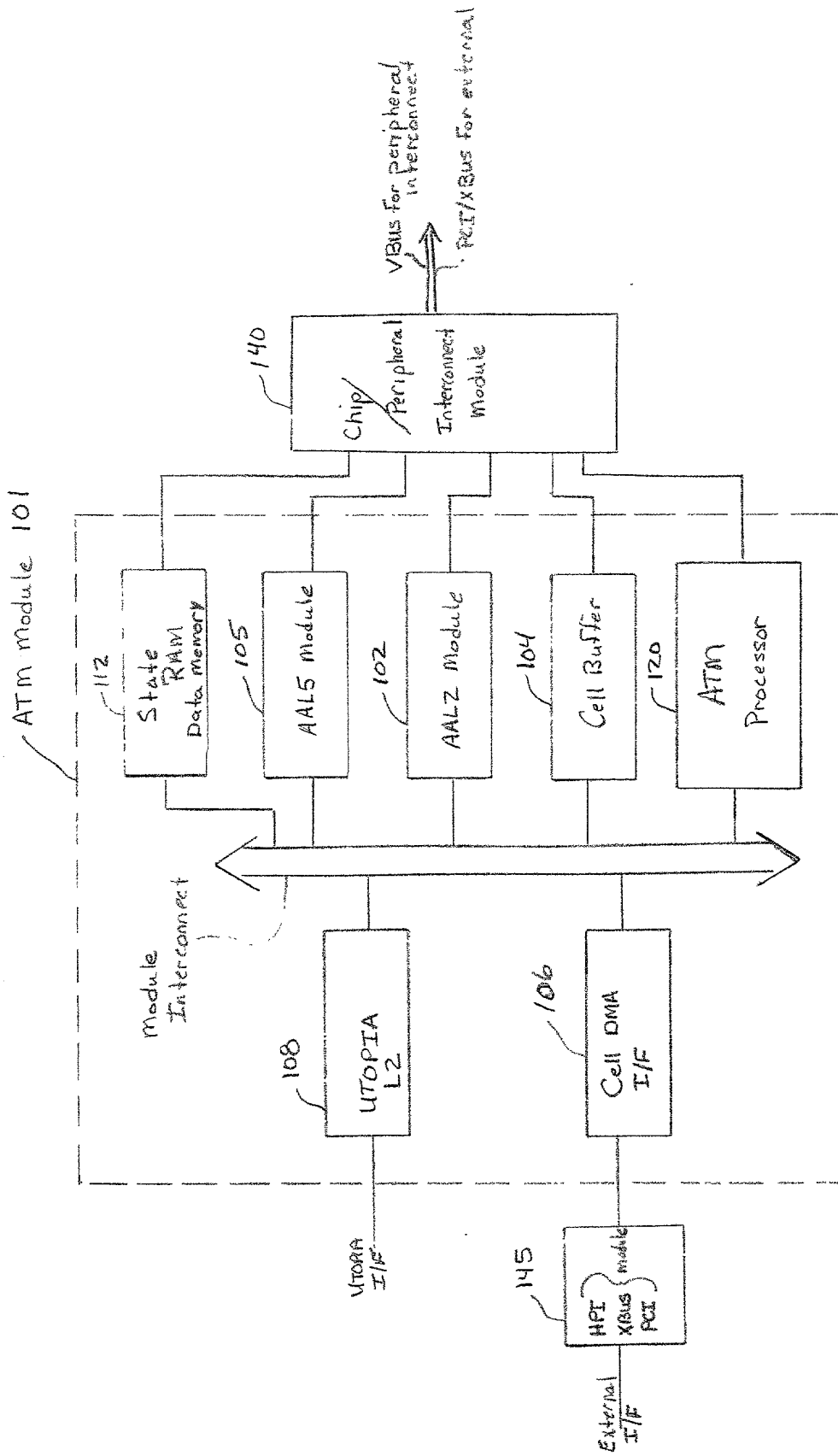


Figure 2

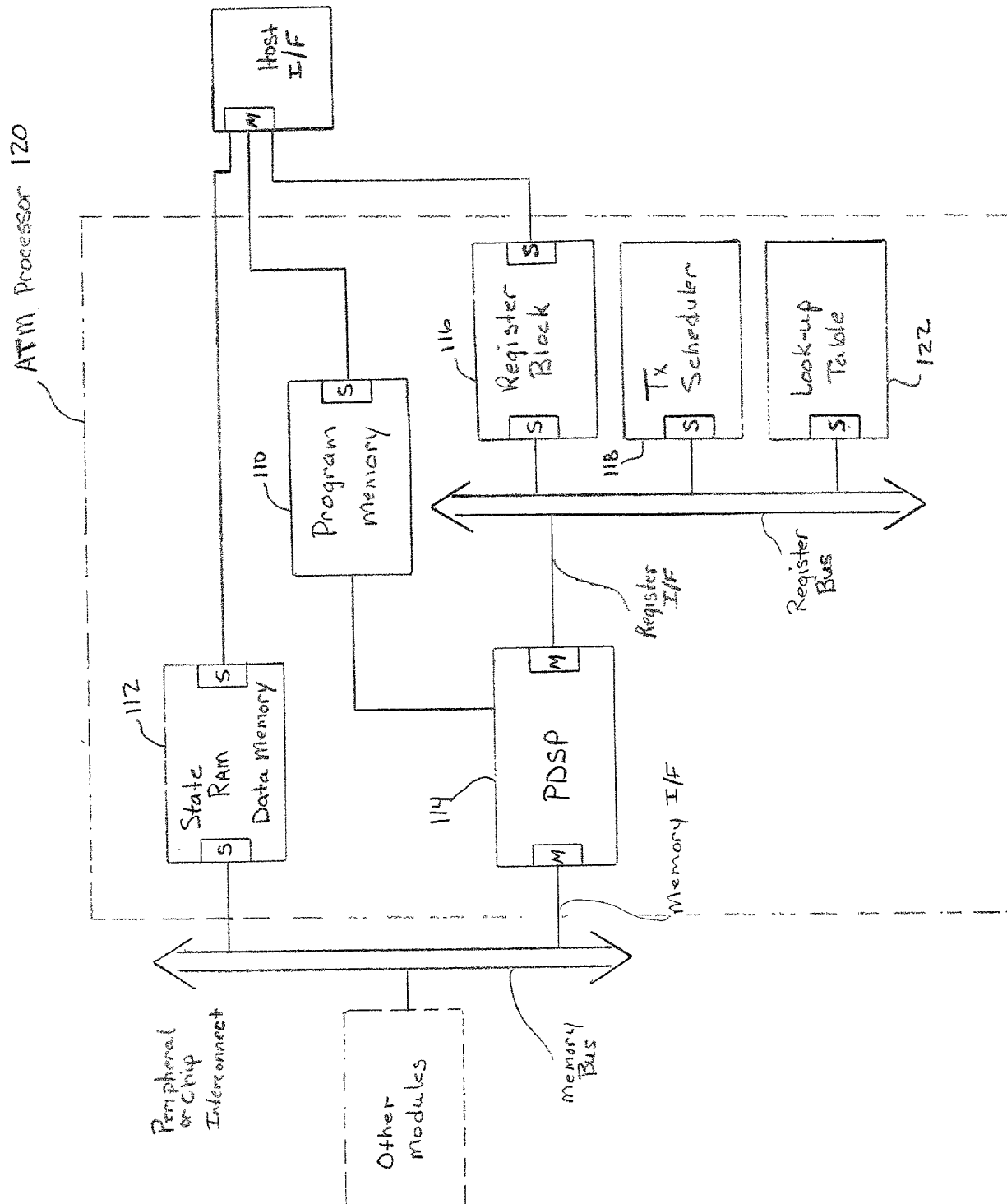


Figure 3

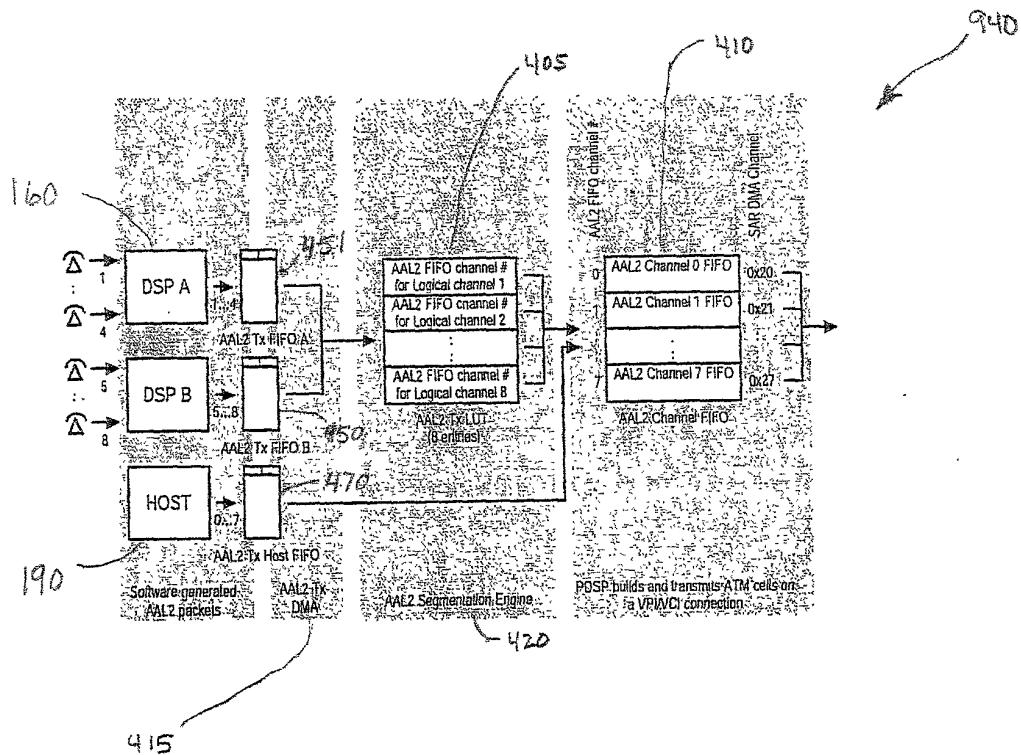


Figure 4

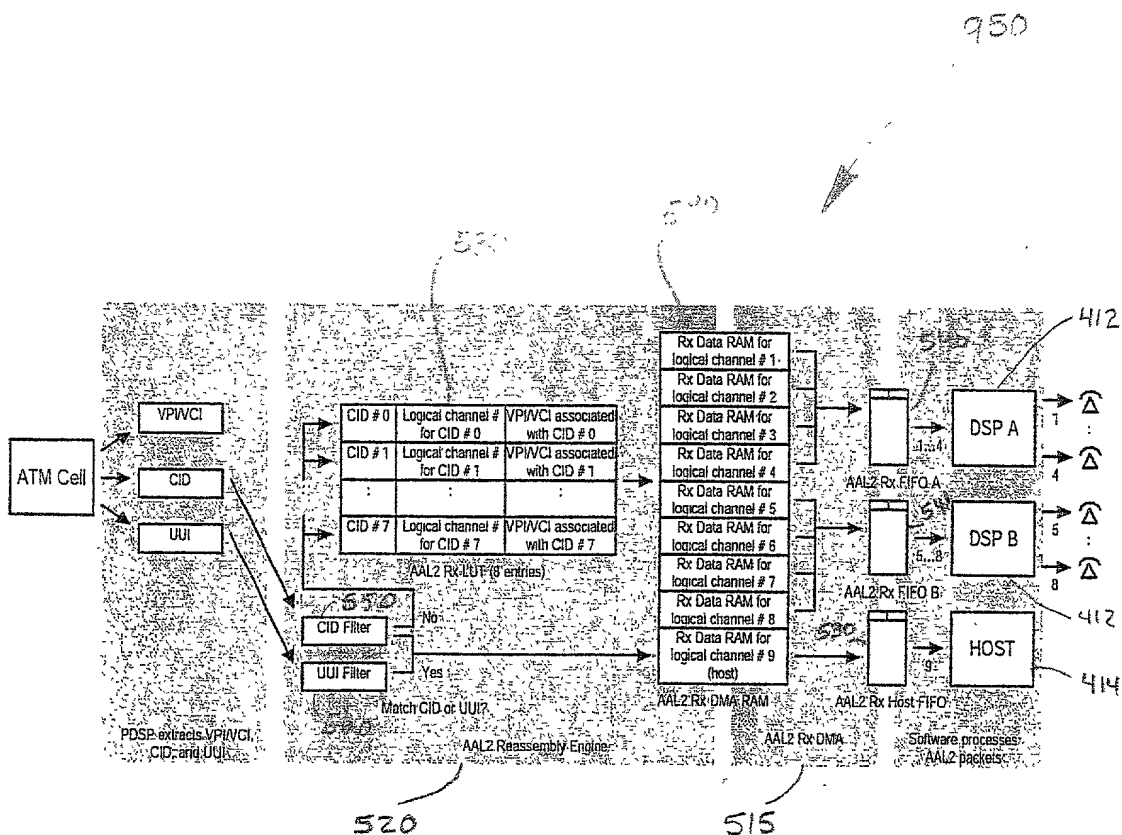


Figure 5

56-byte Aligned ATM cell

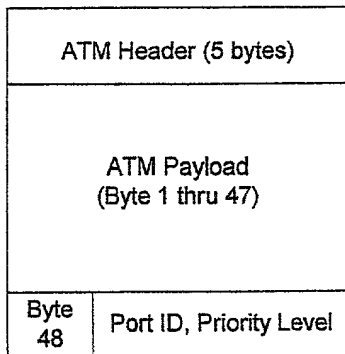


Figure 7

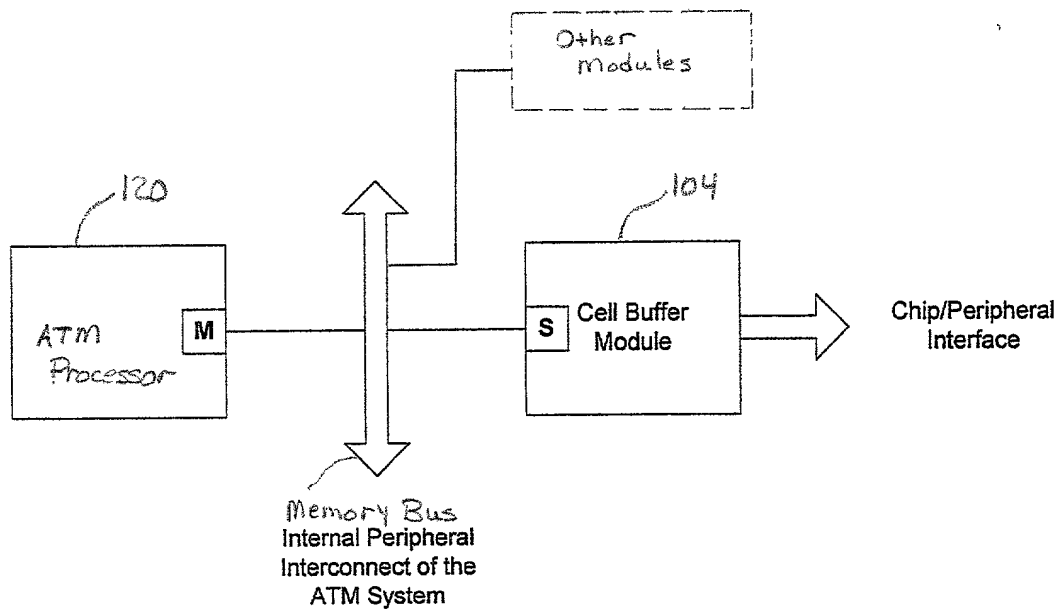


Figure 6A

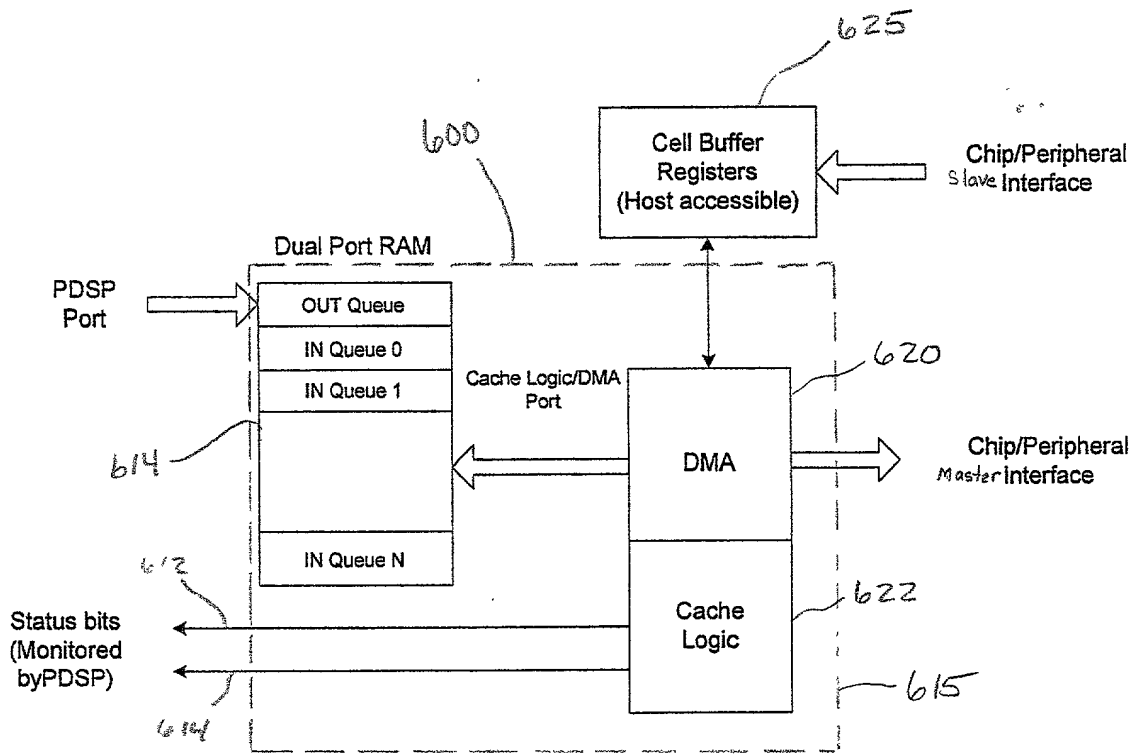


Figure 6B

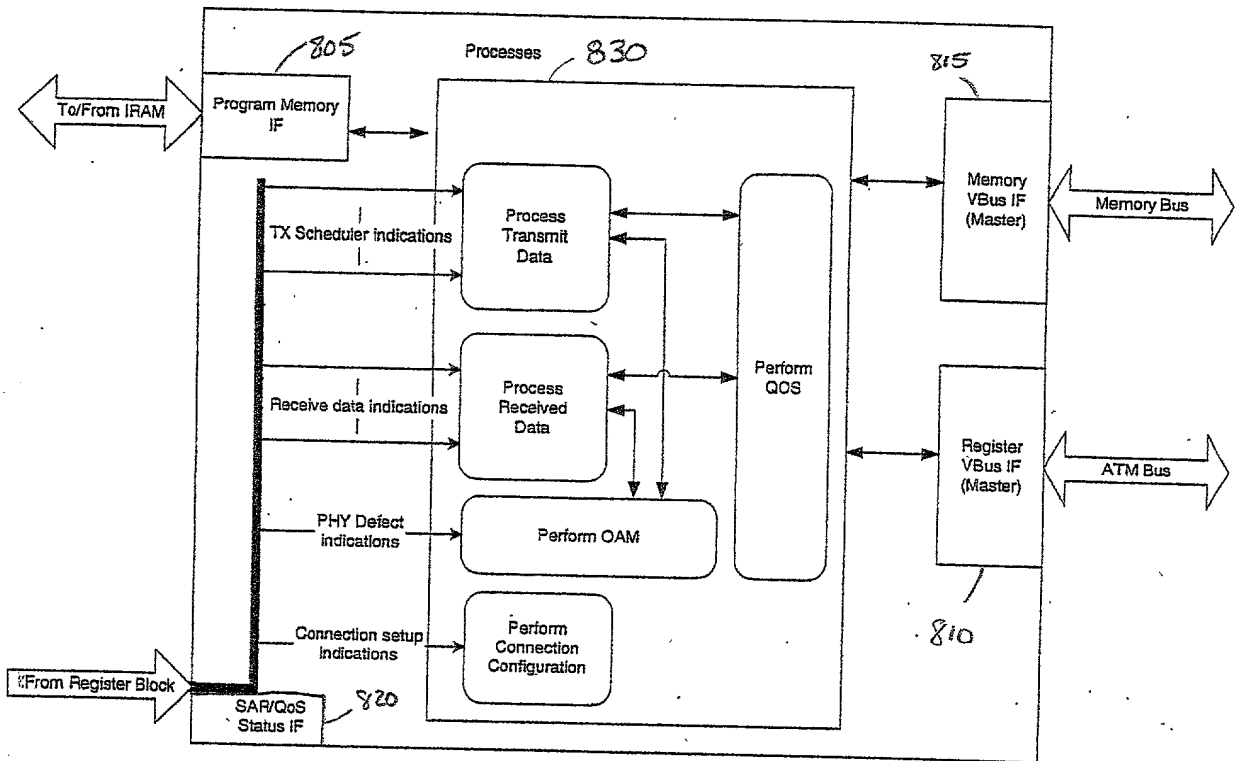


Figure 8